

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	198	((DYNAMICALLY ADJ RECONFIGURABLE) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
L2	4732	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
L3	2789	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR))	USPAT	OR	OFF	2005/04/08 17:34
L4	84	((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET))	USPAT	OR	OFF	2005/04/08 17:34
L5	47	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) AND (INSTRUCTION ADJ SETS))	USPAT	OR	OFF	2005/04/08 17:34
L6	4732	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
L7	84	((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET))	USPAT	OR	OFF	2005/04/08 17:34
L8	2	geographic adj address same local adj address	USPAT	OR	OFF	2005/04/08 17:34
L9	18	(device or computer or controller) adj address same (local adj address)same memory	USPAT	OR	OFF	2005/04/08 17:35
L10	3	geographic adj address same local adj address	US-PGPUB; USPAT	OR	OFF	2005/04/08 17:35
L11	16	baxter.in. and Ricoh.as.	US-PGPUB; USPAT	OR	OFF	2005/04/08 17:35
L12	14	(device or computer or controller) adj address same (local adj address) and @ad<"19980226"	USPAT	OR	OFF	2005/04/08 17:35
S1	94	((DYNAMICALLY ADJ RECONFIGURABLE) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
S2	1216	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
S3	543	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR))	USPAT	OR	OFF	2005/04/08 17:34
S4	14	((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET))	USPAT	OR	OFF	2005/04/08 17:34

S5	9	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) AND (INSTRUCTION ADJ SETS))	USPAT	OR	OFF	2005/04/08 17:34
S6	1	((("5794062".PN.) AND (INSTRUCTION ADJ SET))	USPAT	OR	OFF	2000/03/21 10:52
S7	1	((("5684980".PN.) AND ARCHITECTURE)	USPAT	OR	OFF	2000/03/21 10:52
S8	186	((DYNAMICALLY ADJ RECONFIGURABLE) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2004/12/01 18:33
S9	4396	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3))	USPAT	OR	OFF	2005/04/08 17:34
S10	2563	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR))	USPAT	OR	OFF	2004/12/01 18:33
S11	81	((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET))	USPAT	OR	OFF	2005/04/08 17:34
S12	45	((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) AND (INSTRUCTION ADJ SETS))	USPAT	OR	OFF	2004/12/01 18:33
S13	262471	geographic adj address same local address	USPAT	OR	OFF	2004/12/01 18:34
S14	2	geographic adj address same local adj address	USPAT	OR	OFF	2005/04/08 17:34
S15	27	(device or computer or controller) adj address same (local adj address)	USPAT	OR	OFF	2004/12/02 10:52
S16	18	(device or computer or controller) adj address same (local adj address)same memory	USPAT	OR	OFF	2005/04/08 17:34
S17	3	geographic adj address same local adj address	US-PGPUB; USPAT	OR	OFF	2005/04/08 17:35
S18	2	meta adj address and baxter.in.	US-PGPUB; USPAT	OR	OFF	2004/12/02 10:48
S19	16	baxter.in. and Ricoh.as.	US-PGPUB; USPAT	OR	OFF	2005/04/08 17:35
S20	14	(device or computer or controller) adj address same (local adj address) and @ad<"19980226"	USPAT	OR	OFF	2005/04/08 17:35
S21	6	("4791603" "5109353" "5400262" "5452231" "5535342" "5625580").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/02 11:00